

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200308288-3	APPLICATION NO. 10/680,375	CONFIRMATION NO.
APPLICANT Joel S. Emer et al.			
FILING DATE		GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
KK	1A	5,790,851	08/04/98	Frank et al.	718/104
KK	1B	5,524,247	06/04/96	Mizuno	710/200
KK	1C	5,546,593	08/13/96	Kimura et al.	712/228
KK	1D	5,961,584	10/05/99	Wolf	718/103
KK	1E	5,937,187	08/10/99	Kosche et al.	718/104
KK	1F	5,872,963	02/16/99	Bitar et al.	712/233
KK	1G	6,256,659	07/03/01	McLain, Jr. et al.	718/100
KK	1H	4,642,756	02/10/87	Sherrod	718/103
KK	1I	4,710,883	12/02/87	Wilson et al.	705/410
KK	1J	6,128,640	10/03/00	Kleinman	718/102
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
KK	1L	EP0511748 A2	04/92	IBM Corporation	- / -	
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

KK	1Q	Tullsen, D. M., et al., "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor," Paper Presented at the Proceedings of the 23rd Annual International Symposium on Computer Architecture, Philadelphia, PA, (May 1996).
KK	1R	Lo, J.L., et al., "Converting Thread-Level Parallelism to Instruction-level Parallelism via Simultaneous Multithreading," Version of this Paper to appear in ACM Transactions on Computer Systems, pp. 1-25 (August 1997).
KK	1S	Tullsen, D.M., et al., "Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor," UCSD CSE Technical Report (CS98-587) and UW CSE Technical Report (UW-CSE-98-06-02) (June 1998).

EXAMINER /Kenneth Kim/

DATE CONSIDERED
06/19/2006

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Sheet 2 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200308288-3	APPLICATION NO. 10/680,375	CONFIRMATION NO.
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EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
		2A			
		2B			
		2C			
		2D			
		2E			
		2F			
		2G			
		2H			
		2I			
		2J			
		2K			

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		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
		2L				
		2M				
		2N				
		2O				
		2P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

KK	2Q	Tullsen, D. M. et al., "Simultaneous Multithreading: Maximizing On-Chip Parallelism," Paper Presented at the Proceedings of the 22nd Annual International Symposium on Computer Architecture, Santa Margherita Ligure, Italy (June, 1995).
KK	2R	Eggers, S. J. et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, pps. 12-19, (September/October 1997).
	2S	

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